



## Trainee Program – Analog IC Design

The Fraunhofer-Gesellschaft ([www.fraunhofer.com](http://www.fraunhofer.com)) currently operates 76 institutes and research institutions throughout Germany and is the world's leading applied research organization. Around 32 000 employees work with an annual research budget of 3.4 billion euros.

In the »**Integrated Sensor Systems**« department at the Fraunhofer Institute for Integrated Circuits IIS, we work on a wide range of sensor technologies for a variety of applications. For example, we are developing multispectral image sensors that can be used in agriculture to detect weeds. This enables selective pest control and saves on pesticides. Our magnetic field sensors are used in washing machines, for example, to detect imbalances. This way, they ensure that water and detergents are saved. Our innovative wide-bandgap semiconductor sensors enable highly sensitive environmental sensors for measuring air quality.

**Are you at the beginning of your career and enthusiastic about analog IC design?  
Then join our trainee program to learn directly from leading experts!**

### What you will do

As a trainee in our Analog IC Design program, you will participate in a comprehensive introduction to Analog IC Design, collaborating on real projects under the guidance of experienced engineers. You will gain hands-on experience in the design, simulation, layout, and verification of analog integrated circuits, focusing on optimizing circuits for performance, speed, and area efficiency. Additionally, you will have the opportunity to collaborate with other teams such as layout, test, and product management, fostering a collaborative work environment and gaining exposure to various aspects of semiconductor design.

### What you bring to the table

- Master's degree in electrical engineering or related fields such as physics, mathematics or medical engineering
- Team spirit, communication skills, and ability to work independently

### Ideally:

- Completed Study with a focus on Microelectronics
- Knowledge of circuit technology
- Experience with design tools such as Cadence or Synopsys is advantageous

### What you can expect

Fraunhofer is not only the largest organization for applied research in Europe, but also a **top-rated employer**. [How so?](#)

- Fraunhofer IIS is one of **Europe's leading R&D IC design facilities**
- An individually tailored trainee program that combines **personal and professional development**
- Insight into **innovative technologies** and state-of-the-art design methodologies We work with state-of-the-art equipment in a highly innovative key industry. You benefit from a **unique network** of experts and partners in research and industry.
- We take your needs seriously: We support the best possible work-life balance through **flexible working hours and hybrid working**.
- Your goals and interests are important to us: We support you with a **mentoring program**.

- We enable **equal opportunities**: For example, our female scientists are supported by the »Fraunhofer TALENTA« career program.

The position is initially limited to 6 months with the aim to extend it subsequently. We value and promote the diversity of our employees' skills and welcome all applications - regardless of age, gender, nationality, ethnic and social origin, religion, ideology, disability, sexual orientation, and identity. Appointment, remuneration, and social security benefits are based on the public-sector collective wage agreement (TVöD).

With its focus on developing key technologies that are vital for the future and enabling the commercial utilization of this work by business and industry, Fraunhofer plays a central role in the innovation process. As a pioneer and catalyst for groundbreaking developments and scientific excellence, Fraunhofer helps shape society now and in the future.

**Interested? If you are ready to advance your career in Analog IC Design and be part of a highly skilled team, then apply online now with your application documents (cover letter, CV, references). We look forward to getting to know you!**

Yevgeniy Itskovych

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[www.iis.fraunhofer.de](http://www.iis.fraunhofer.de)

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