



Senior Analog IC-Designer for R&D of Integrated Sensors and Systems

The Fraunhofer-Gesellschaft (www.fraunhofer.de) currently operates 76 institutes and research facilities in Germany and is the world's leading organization for application-oriented research. Around 32,000 employees work on the annual research volume of 3.4 billion euros.

In our [»Integrated Sensor Systems«](#) department at the Fraunhofer Institute for Integrated Circuits IIS, we work on a wide range of sensor technologies for a variety of applications. For example, we are developing **multispectral image sensors** that are used in agriculture to detect weeds. This enables selective control and saves on pesticides. Our **magnetic field sensors** are used in washing machines, for example, to help save water and detergent through precise position measurement. Innovative AlGaIn/GaN sensors enable new sensor principles for even more sensitive sensors, which consistently expand our established fields of application.

Do you have experience in analog circuit design and would like to apply your knowledge to our innovative technologies and make a contribution to application-oriented research? Would you like to pass on your experience in IC design to young professionals and convey the fascination of circuit development through our exciting applications? Then this position is worth a look!

What you will do

You will primarily design analog circuits for complex mixed-signal ASICs and integrated sensor systems. A particular focus lies on low-power circuits for integrated magnetic and optical sensors. With your experience, you can contribute constructively in various project phases from conceptualization to implementation and participate in the layout of critical circuit parts through to the evaluation of prototypes.

You will also use your experience to support newcomers in their personal development as professional IC designers. Together with young colleagues, you will work in a team on the specification, design and verification of ASICs and typical function blocks such as PGAs and ADCs.

In addition, you will support or take over the technical project management in development and research projects in the field of IC design. In your projects, you will be supported by our team in project management, evaluation and series transfer.

What you bring to the table

- Completed scientific university degree in electrical engineering, physics or a comparable field of study
- At least 4 years of professional experience in circuit development
- Sound knowledge of analog or mixed-signal circuit design
- Experience in the design, simulation and layout of integrated circuits with Cadence
- High degree of independence and personal responsibility
- Good command of written and spoken English and preferably German

The following is also an advantage

- Experience in technical project management

What you can expect

Fraunhofer is not only the largest organization for application-oriented research in Europe, but we are also considered a top employer. [But why?](#)

- Fraunhofer IIS is also one of **Europe's leading IC design institutes**.
- We offer a wide range of **excellent training and continuing education** opportunities (internal & external).
- You will have opportunities for **scientific exchange** within and outside the institute, e.g. through active participation in congresses and conferences.
- We work with **state-of-the-art equipment** in a highly innovative key industry. You will benefit from a **unique network** of experts and partners in research and industry.
- You will have the opportunity to **contribute your creativity to pioneering research projects** and pass on your knowledge to motivated junior staff.

We value and promote the diversity of our employees' skills and therefore welcome all applications - regardless of age, gender, nationality, ethnic and social background, religion, ideology, disability, sexual orientation and identity.

The weekly working time is 39 hours. The position can also be filled part-time. The position is initially limited to two years. An extension is possible. Employment, remuneration and social benefits are based on the collective agreement for the public sector (TVöD). In addition, Fraunhofer may grant performance- and success-based variable remuneration components.

The Fraunhofer-Gesellschaft plays a central role in the innovation process by focusing on key technologies of relevance to the future and on the utilization of the results in business and industry. As a trailblazer and driving force for innovative developments and scientific excellence, it helps to shape our society and our future.

Have we piqued your interest?

Then apply [online](#) now with your application documents (cover letter, CV, references). We look forward to getting to know you!

Luca Marie Prietz
Fraunhofer Institute for Integrated Circuits IIS

www.iis.fraunhofer.de/en

Kennziffer: 1827932

Bewerbungsfrist: Keine

Standort: Erlangen

